

Attorney Docket No. 0756-2131

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 09/535,015

Filed: March 24, 2000

For: METHOD OF MANUFACTURING A  
SEMICONDUCTOR DEVICE

) Group Art Unit: 2811

) Examiner: S. Crane

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Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c).

Respectfully submitted,

Eric J. Robinson  
Reg. No. 38,285

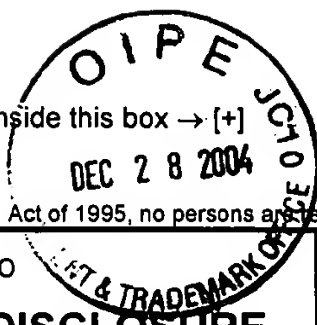
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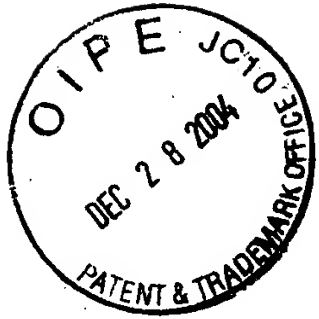
<b>Substitute for form 1449A/PTO</b> <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	09/535,015
				Filing Date	March 24, 2000
				First Named Inventor	Shunpei YAMAZAKI et al.
				Group Art Unit	2811
				Examiner Name	S. Crane
Sheet	1	of	1	Attorney Docket Number	0756-2131

U.S. PATENT DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
		5,531,862		Otsubo et al.	07/02/1996	
		5,575,883		Nishikawa	11/19/1996	
		5,704,986		Chen et al.	01/06/1998	
		5,902,993		Okushiba et al.	05/11/1999	
		5,970,327		Makita et al.	10/19/1999	
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		6,162,704		Yamazaki et al.	12/19/2000	
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		6,294,441		Yamazaki	09/25/2001	
		6,461,943		Yamazaki et al.	10/08/2002	
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		6,495,404		Teramoto et al.	12/17/2002	
		6,528,358		Yamazaki et al.	03/04/2003	
		6,620,711		Yamazaki	09/16/2003	

FOREIGN PATENT DOCUMENTS								
Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Office <sup>3</sup>	Number <sup>4</sup>	Kind Code <sup>5</sup> (if known)				
		JP	08-045839			02/16/1996		Abst.
		JP	08-045840			02/16/1996		Abst.
		JP	08-097169			04/12/1996		Abst.

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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



This will acknowledge receipt of the following:

1. Information Disclosure Statement with Certificate of Mailing
2. Form PTO-1449

in re PATENT application of:

Shunpei YAMAZAKI et al.

Serial No. 09/535,015

Filed: 03/24/2000

Title: METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

Due date: 09/15/2000

0756-2131

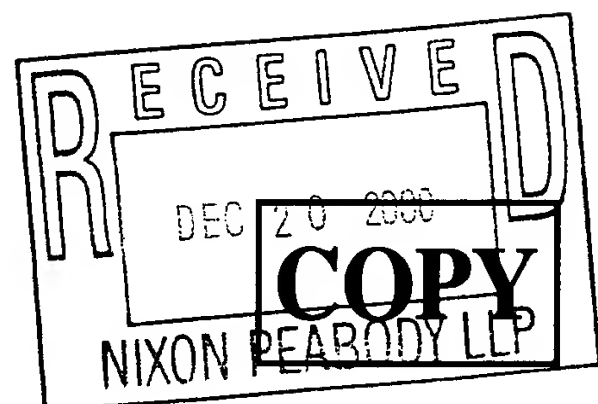
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of: )  
 Shunpei YAMAZAKI et al. ) Art Unit: 2825  
 Serial No. 09/535,015 ) Examiner: Unassigned  
 Filed: 03/24/2000 )  
 For: METHOD OF MANUFACTURING A )  
 SEMICONDUCTOR DEVICE )

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
 Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the reference(s) listed on the attached Form PTO-1449 be made of record in the above-identified application. Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The following two articles were cited by the Examiner in the following Applications:

*Kuo, "Thin Film Transistor Technologies", Vol. 94-35, PP. 116-122, The Electrochemical Society Proceedings, cited in Serial No's 08/784,292 and 09/233,450.*

*"Thermo Auto-Chrome Full Color Recording Technology", May 31, 1995, Technology Information Association, Patent No. 6,093,934, Serial No. 08/785,536.*

The Commissioner is hereby authorized to charge fees under 37 C.F.R. §§1.16, 1.17, 1.20(a), 1.20(b), 1.20(c), and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

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Respectfully submitted,



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Eric J. Robinson  
Reg. No. 38,285

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8180 Greensboro Drive, Suite 800  
McLean, Virginia 22102  
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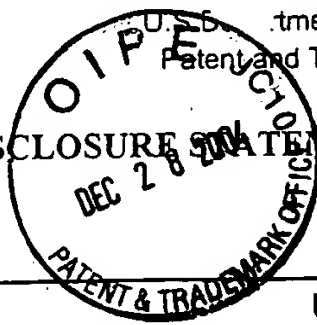
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Filing Date: March 24, 2000

Group Art Unit: 2825



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	3,783,049	01/01/1974	Sandera			
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	4,277,884	07/14/1981	Hsu			
	4,300,989	11/17/1981	Chang			
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	4,409,724	10/18/1983	Tasch, Jr. et al.			
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	4,544,418	10/01/1985	Gibbons			
	4,546,376	10/08/1985	Nakata et al.			
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	4,634,473	01/06/1987	Swartz et al.			
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	4,755,481	07/05/1988	Faraone			
	4,911,781	03/27/1990	Fox et al.			
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	4,996,077	02/26/1991	Moslehi et al.			
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Atty Docket 0756-2131

Serial No. 09/535,015

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Applicants: Shupey YAMAZAKI et al.

Filing Date: March 24, 2000

Group Art Unit: 2825

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
	5,089,441	02/18/1992	Moslehi			
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	5,254,480	10/19/1993	Tran			
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Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
	5,550,070	08/27/1996	Funai et al.			
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	5,619,044	04/08/1997	Makita et al.			
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	5,684,317	11/04/1997	Hwang			
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## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date (if appropriate)
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	6,011,277	01/04/2000	Yamazaki			
	6,013,929	01/11/2000	Ohtani			
	6,048,758	04/11/2000	Yamazaki et al.			
	6,063,654	05/16/2000	Ohtani			
	6,077,731	06/20/2000	Yamazaki et al.			
	6,093,934	07/25/2000	Yamazaki et al.			
	6,100,562	08/08/2000	Yamazaki et al.			
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	RE 28,386	04/08/1975	Heiman et al.			

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Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	0 178 447	10/09/1984	EP			Engl.	
	01-187814	07/27/1989	Japan			Full	
	01-187874	07/27/1989	Japan			Abst.	
	01-187875	07/27/1989	Japan			Abst.	
	01-206632	08/18/1989	Japan			Abst.	
	02-140915	05/30/1990	Japan			Full	
	02-148687	06/07/1990	Japan			Abst.	
	02-275641	11/09/1990	Japan			Abst.	
	03-280418	12/11/1991	Japan			Abst.	
	03-280420	12/11/1991	Japan			Full	
	05-082442	04/02/1993	Japan			Abst.	
	05-107561	04/30/1993	Japan			Abst.	
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	06-232059	08/19/1994	Japan			Full	
	07-161634	06/23/1995	Japan			Abst.	
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	08-129358	05/21/1996	Japan			Abst.	
	08-129359	05/21/1996	Japan			Abst.	

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Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
	08-129360	05/21/1996	Japan			Abst.	
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	08-241047	09/17/1996	Japan			Abst.	
	08-241048	09/17/1996	Japan			Abst.	
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	08-241997	09/17/1996	Japan			Abst.	
	60-105216	06/10/1985	Japan			Full	
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## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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	Wolf et al., "Silicon Processing for the VLSI Era Volume 1: Process Technology", 1986, pp. 215-216, Lattice Press
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